

Walk in Interview 2nd January 2014

UGC-JRF

Applications are invited for the post of one (1) project fellow to work in an UGC project entitled “**Study of quantum dot cellular automata for designing circuits and implementing them for high speed low power fault tolerant computing**”.

PI: DR.DEBASHIS DE, ASSOCIATE PROFESSOR, CSE, WBUT

EMAIL: dr.debashis.de@gmail.com

Mobile: 9830363215

Date and Time: 02/01/2014, Room no 315., 2PM

Fellowship:

Emoluments will be Rs. 14,000 p.m. for the first year and Rs. 16,000 p.m. for the second year , on a consolidated basis as per ugc norms. Appointment will co- terminus with the project.

Eligibility:

Candidates must be M.Tech. in Computer Science and Engg./Information Technology/Electronics and Communication Engineering/VLSI or equivalent degree. Knowledge of Digital Electronics, Quantum mechanics, VLSI circuit design, computer architecture and experience of working with scientific software packages like design tools(like Matlab, Mathematica and related softwares) is desirable.